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(71) Applicant: **mitsubishi electric corp**

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(72) Inventor: **ONISHI KENJI**

(54) **NON-VOLATILE MEMORY, PULSE WIDTH
DECIDING METHOD OF WRITING PULSE IN
NON-VOLATILE MEMORY, PULSE WIDTH
DECIDING METHOD OF ERASING PULSE IN
NON-VOLATILE MEMORY, AND BUILT-IN
MICROCOMPUTER OF NON-VOLATILE MEMORY**

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(57) Abstract:

PROBLEM TO BE SOLVED: To provide an inexpensive non-volatile memory disposing with a generating circuit of an independent clock signal used exclusively for a non-volatile memory.

SOLUTION: This device is constituted of a pulse width storing means 16 storing pulse width data of a writing pulse, a pulse width writing means 2 writing data in the pulse width storing means 16, a clock supplying means 5 supplying a clock signal CK1, a clock frequency-dividing means 15 frequency-dividing the clock signal CK1 based on pulse width data of the pulse width storing means 16 and outputting a clock signal CK2, and a pulse generating means 13 generating a timing signal of a writing pulse based on the clock signal CK2.

